Specifications

Power Consumption		
+5V	30 mA Typical	69 mA Max during normal operation
	150mW	345mW
	59mA Typical	98mA Max (during CIS read only)
Analog Input		
Channels	8 Single Ended	
Resolution	12 bits, 1 part in 4096	
Accuracy	+/- 1/2 LSB	
Input Range	+/-5V May be customized at time of manufacture.	
Convert & Transfer Rate	25KHz Max	
Internal Pacer	25KHz, 12.5KHz, 6.25KHz, 3.125KHz software selectable.	
Coding	Bipolar	Offset Binary
Overvoltage	+/- 15V	
Input Impedance	>10M Ohm	
Model	ADS574 (Burr Brown)	
Type	Successive Approximation	
Conversion Time	25uS	
Temp. Coef.	Full Scale	+/- 45 ppm/C
	Offset	+/- 10 ppm/C
Linearity	+/- 1 LSB	
Digital I/O		
Input	3 Bits, DI3 doubles as External Trigger for A/D	
Input Low Volts	0.8V	
Input High Volts	2.0V	
Output	3 Bits Latched	
Output Low Volts	0.23V Max @ IOL = 4.0mA	
Output High Volts	3.86V Max @ IOH = -4.0mA	
Interrupt		
Level	2-5, 7, 9, 10, 15 set by CB.CFG file read by CBCLIENT.EXE	
Enable	Programmable. See register base + 3	
Trigger	External or internal	
Type	Rising edge not to exceed TTL specifications	